

INFORMATION DISCLOSURE CITATION
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ATTY DOCKET NO.
BU9-98-050DIV2

SERIAL NO.
Not yet assigned

Moore et al.

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	3,700,873	10/24/72	Yhap	708	100	
	3,918,030	11/04/75	Walker	712	212	
	4,145,736	03/20/79	Yamada et al.	712	212	
	4,251,862	02/17/81	Murayama	712	212	
	4,594,661	06/10/86	Moore et al.	712	248	
	4,660,171	04/21/87	Moore et al.	712	212	
	4,685,080	08/04/87	Rhodes, Jr. et al.	712	248	
	4,763,246	08/09/88	Holt et al.	712	212	
	5,084,824	01/28/92	Lam et al.	716	11	
	5,093,775	03/03/92	Grundmann et al.	712	212	
	5,185,868	02/09/93	Tran	712	217	

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	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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3-NOVEMBER-2003

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BU9-98-050D1V2

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,249,273	09/28/93	Yoshitake et al.	712	210	
	5,276,824	01/04/94	Shruhak et al.	712	212	
	5,299,321	03/29/94	Iizuka	712	212	
	5,371,864	12/06/94	Chuang	712	206	
	5,412,785	05/02/95	Skruhak et al.	712	230	
	5,426,591	06/20/95	Ginetti et al.	716	6	
	5,455,938	10/03/95	Ahmed	716	5	-
	5,465,216	11/07/95	Rotem et al.	716	5	
	5,475,824	12/12/95	Grochowski et al.	712	206	
	5,491,640	02/13/96	Sharma et al.	716	18	
	5,515,302	05/07/96	Horr et al.	358	1.15	

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						YES	NO

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INFORMATION DISCLOSURE CITATION

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,517,432	05/14/96	Chandra et al.	703	14	
	5,519,627	05/21/96	Mahmood et al.	716	18	
	5,568,442	10/22/96	Kowalczyk et al.	365	230.03	
	5,572,437	11/05/96	Rostoker et al.	716	18	
	5,581,742	12/03/96	Lin et al.	716	4	
	5,598,348	01/28/97	Rusu et al.	716	21	
	5,615,357	03/25/97	Ball	703	21	
	5,650,938	07/22/97	Bootehsaz et al.	716	6	
	5,680,332	10/21/97	Raimi et al.	703	13	
	5,732,255	03/1998	Verbauwhede	712	248	

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	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

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Su et al., "Low Power Architecture Design and Compilation Techniques for High-Performance Processors", IEEE, 1994, pp. 489-498.

Su et al., "Saving Power in the control Path of Embedded Processors", IEEE Design & Test of Computers, 1994, pp. 24-30.

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